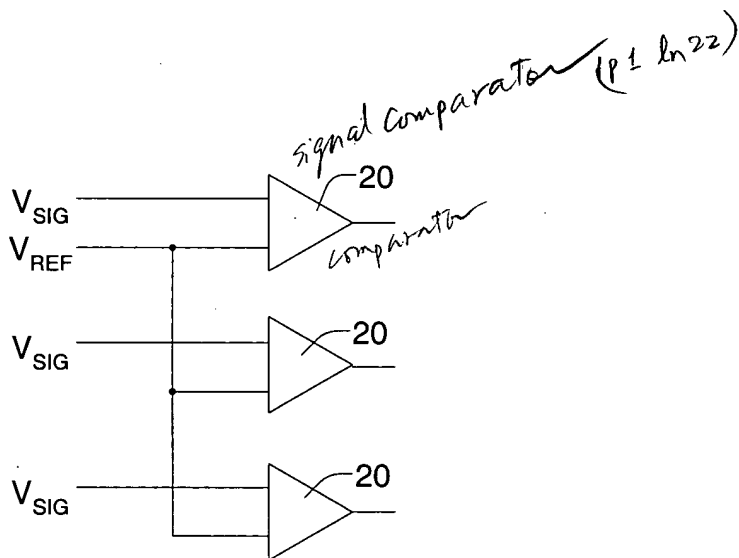
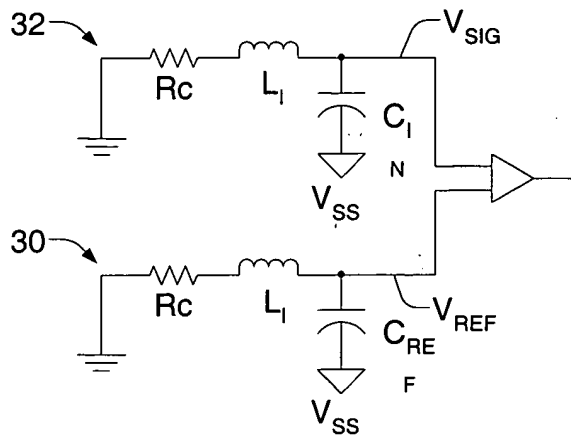


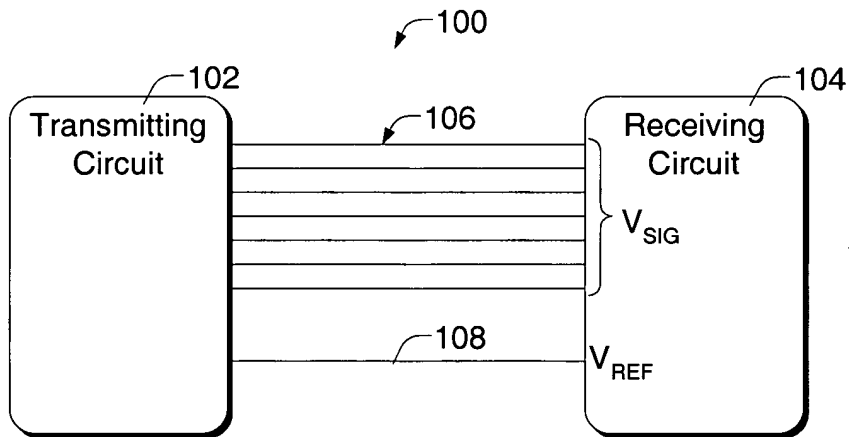
*Fig. 1*  
*Prior Art*



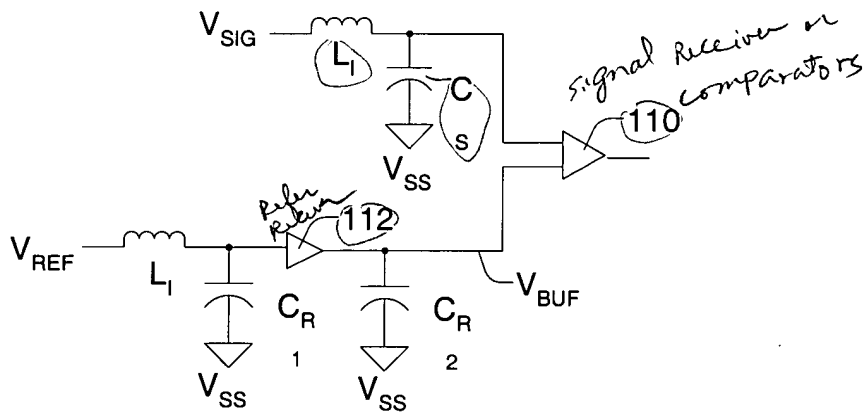
*Fig. 2*  
*Prior Art*



*Fig. 3*  
*Prior Art*

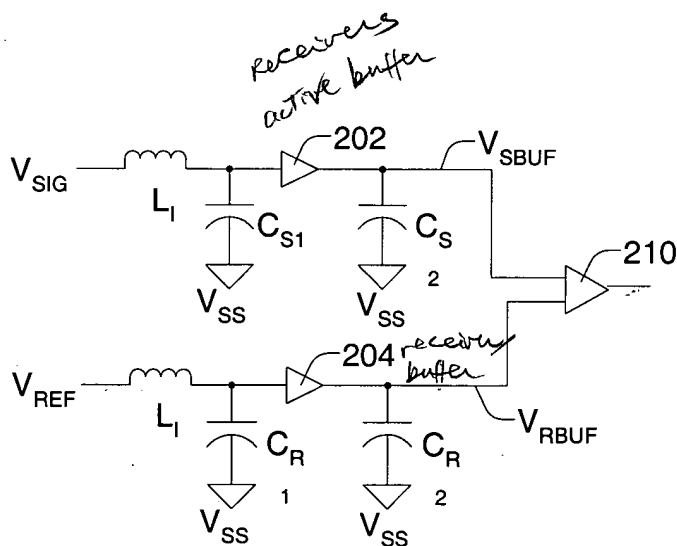


*Fig. 4*



**Fig. 5**

*Receiver circuitry*



**Fig. 6**

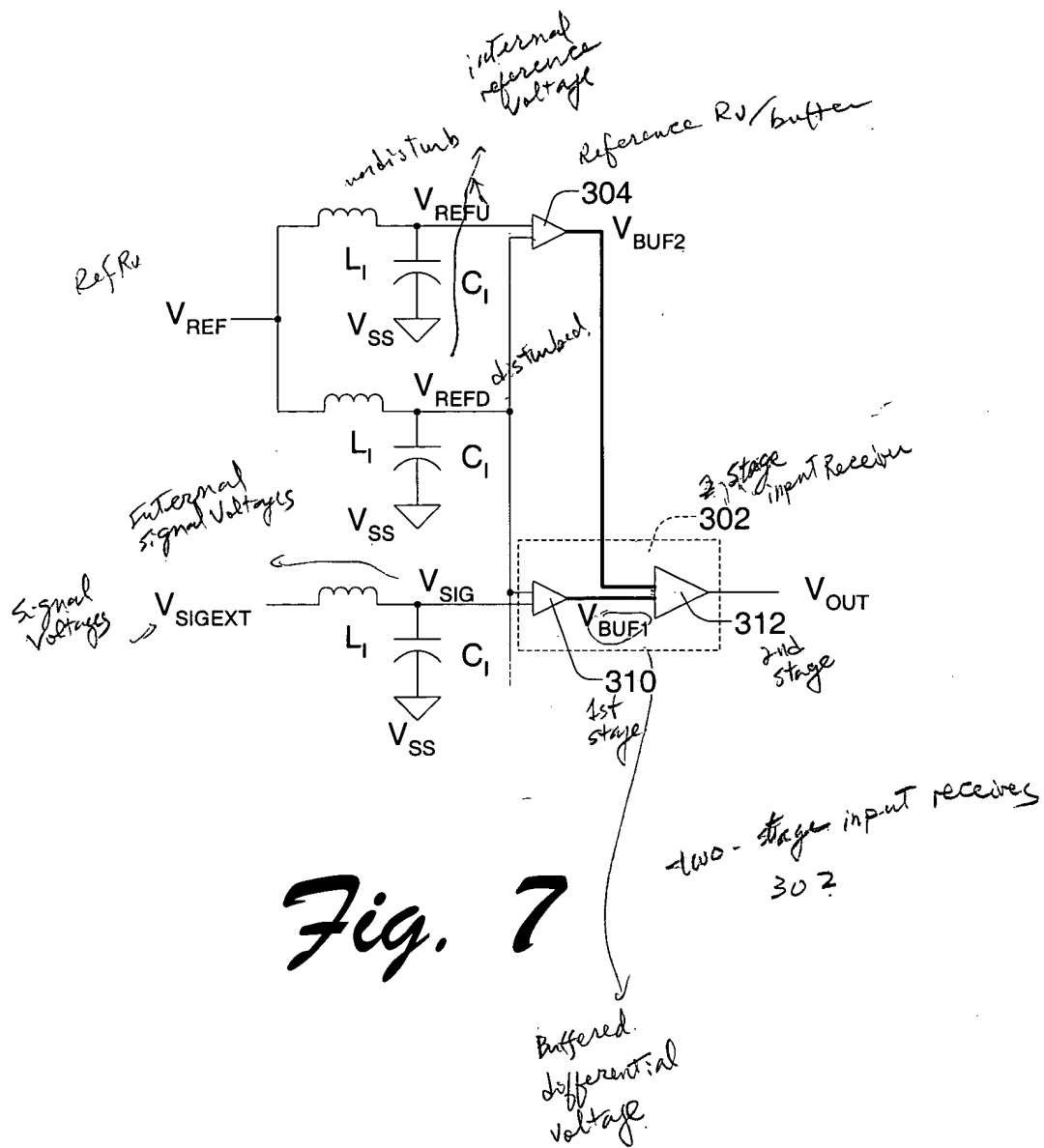


Fig. 7